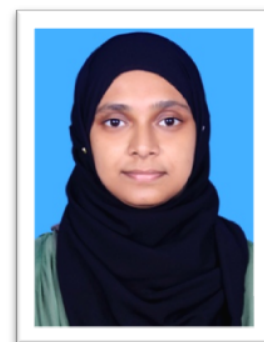


FACULTY-PROFILE

1. Name : TASNEEM SALAM H
2. Date of Birth :
3. Highest Qualifications : M.Tech in VLSI and EMBEDDED SYSTEMS
4. Academic Performance :



(High school to Highest Qualification.)

Sl. No.	Qualification	Board/University	Year of Passing	Specialization	Class /Division
1	SSLC	CBSE	2000		First Class With Distinction
2	Plus-Two	AISSCE	2002		First Class
3	B. Tech	Cochin University Of Science And Technology	2007	Electronics & Communication	First Class
4	M. Tech	Cochin University Of Science And Technology	2016	VLSI and EMBEDDED SYSTEMS	First Class With Distinction

5. Date of Joining : 30-8-2010
6. Status as on date of joining : Lecturer
7. Salary as on date of joining :
8. Present status : Assistant Professor
9. Salary as on date : -----
10. Number of promotions since date of joining : Nil
11. Achievements since date of joining :

11.1 Faculty Development

Sl.No	Name of Training Programme/Workshop	Period & Duration	Organization	Remarks
1	Workshop on Tanner EDA Software	20/11/2014 to 22/11/2014	IEEE Solid State Circuit Society Chapter of College of Engineering Chengannur.	

2	National Workshop on Advanced Nano-Scale Device Design Using TCAD	28/12/2015 to 1/01/2016	IEEE Solid State Circuit Society Chapter of College of Engineering Chengannur.	
3	Workshop on Planning Towards Future Development	03/11/2016	Department of Applied Science College of Engineering ,Kidangoor	
4	International Conference on NextGen Electronic Technologies:Silicon to Software	23/03/2017 to 25/03/2017	School of Electronics Engineering at VIT Chennai,India	
5	FDP on Advanced Communication Technologies and Signal Processing	19/04/2018 to 23/04/2018	Department of Electronics and Communication Engineering ,College of Engineering Perumon	
6	Awareness Training Programme on “Migration to SciLab”,	4/2/2017	organized by the International Centre for Free and Open Source Software (ICFOSS) in association with A P J Abdul Kalam Technological University, at College of Engineering, Trivandrum.	

11.2 No. of Publications

Sl.No.	Type of Publication	Number
1	International Conferences	1
2	National Conferences	-
3	International Journal	-
4	National Journal	-

11.3 Conferences/Seminars/STTP/Workshops

11.3.1 Organized/Attended

Sl.No.	Workshop/conferences	Year & Duration	Sponsors
1	International Conference on NextGen Electronic Technologies:Silicon to Software	23/03/2017 to 25/03/2017	School of Electronics Engineering at VIT

			Chennai, India
--	--	--	----------------

11.3.2 Invited Talks

Sl.No.	Title of Paper	Year & Duration	Sponsors

11.4 Extracurricular Activities

Sl.No.	Particulars	Position Held	Year	Remarks
1.	IEEE	Branch Councillor	2017,2018	
2.	ECE Association	In charge	2011	
3.	Staff Club	Executive Member	2017,2018	

11.8 Administration

Sl.No.	Position Held	Duration	Remarks

2014– 2016

(Under Study Leave on Loss of Pay)

2016 – 2017

(College Of Engineering and Management Punnapra)

1 Details of Innovations/Contributions in Teaching during the year

- (a) Design of curriculum :
- (b) Teaching methods : Using black board and ICT
- (c) Laboratory experiments :Electronic Circuits Lab, Electronics Workshop
- (d) Evaluation methods : Series Tests, Assignments
- (e) Preparation of resource material including books, reading material, lab manuals :Prepared Lab Manual for EC Workshop
- (f) Remedial teaching / Student Counseling (academic) : Conducted Remedial Classes for 1304 Introduction to electronics Engineering

- (g) Academic help rendered to HOD : In-Charge of DSP Lab,
other than class work class tutor of EC 15-19 admn
- (h) Academic/College activities : Department Alumni Co-ordinator,
help rendered to the principal Department IEEE co-ordinator,
other than class work

2 Improvement of Professional Competence

- Attended international conference
- Published an IEEE conference paper
- Attended international conference

2017 – 2018
(College Of Engineering Perumon)

1 Details of Innovations/Contributions in Teaching during the year

- (a) Design of curriculum :
- (b) Teaching methods : Using black board and powerpoint presentations
- (c) Laboratory experiments : EDA lab
- (d) Evaluation methods : Series Tests, Assignments
- (e) Preparation of resource material :
including books, reading material, lab manuals
- (f) Remedial teaching / Student :
Counseling (academic)
- (g) Academic help rendered to HOD : 1Class Advisor for EC 2017-21 Batch
other than class work 2. In-Charge of EDA lab
- (h) Academic/College activities : IEEE branch Councillor
help rendered to the principal Staff club Executive Member
other than class work

2 Improvement of Professional Competence

- Attended Faculty Development Programmes

13. Self appraisal:

- 3 Major Strengths : 1) Dedication in teaching
2) Maintain good student teacher relation
3) Friendly
- 3 Major Weaknesses : 1) Time management

14. Address for communication:

Office: Asst. Professor, Dept. of Electronics & Communication,
College Of Engineering Perumon, Perinad, Kollam

Residence: 169B, South of Vahini Honda, Vadakkevila P O, Pallimukku, 691010

15. Blood Group: O+ve

16. Contact Details

Mail id iamtaaz@gmail.com

Contact number 9400185562

Signature